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**1 Development of a PC-Windows based universal control system***Marsden, G.D.; Robinson, P.;*
 Factory 2000 - The Technology Exploitation Process, Fifth International Conference on (Conf. Publ. No. 435) , 2-4 Apr 1997  
 Page(s): 284 -287

[\[Abstract\]](#) [\[PDF Full-Text \(556 KB\)\]](#) **IEE CNF**
**2 Modular rocket engine control software (MRECS)***Tarrant, C.; Crook, J.;*
 Digital Avionics Systems Conference, 1997. 16th DASC., AIAA/IEEE , Volume: 2 , 26-30 Oct 1997  
 Page(s): 8.3 -24-8.3-30 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(716 KB\)\]](#) **IEEE CNF**
**3 Proposed avionics architecture for Air Force Air Mobility Command aircraft to meet CNS/ATM and GATM requirements***Happel, D.A.;*
 Digital Avionics Systems Conference, 1998. Proceedings., 17th DASC. The AIAA/IEEE/SAE , Volume: 2 , 31 Oct-7 Nov 1998  
 Page(s): F16/1 -F16/9 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(736 KB\)\]](#) **IEEE CNF**
**4 GUARDS: a generic upgradable architecture for real-time**

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(legacy <paragraph> (upgrade <or> retrofit) <paragraph> embedded)

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### 1 Embedded real-time objects: when Java meets CORBA in embedded real-time systems

*Gien, M.; Tombroff, M.;*

WESCON/97. Conference Proceedings, 4-6 Nov 1997

Page(s): 408 -417

[\[Abstract\]](#) [\[PDF Full-Text \(416 KB\)\]](#) **IEEE CNF**

### 2 A commercial off-the-shelf based replacement strategy for aging avionics computers

*Luke, J.; Bittorie, J.W.; Cannon, W.J.; Haldeman, D.G.;*

Aerospace and Electronics Conference, 1998. NAECON 1998.

Proceedings of the IEEE 1998 National, 13-17 Jul 1998

Page(s): 177 -181

[\[Abstract\]](#) [\[PDF Full-Text \(584 KB\)\]](#) **IEEE CNF**

### 3 Embedded diagnostics for legacy processor based systems

*Navarro, S.A.; Haldeman, D.G.; Vicen, P.;*

AUTOTESTCON '98. IEEE Systems Readiness Technology

Conference., 1998 IEEE, 24-27 Aug 1998

Page(s): 530 -535

[\[Abstract\]](#) [\[PDF Full-Text \(748 KB\)\]](#) **IEEE CNF**

### 4 Star tool: reengineering for Ada and C

*Boettcher, C.;*

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### 1 Synthesis of interfaces and communication in reconfigurable embedded systems

*Eisenring, M.; Platzner, M.;*

Computers and Digital Techniques, IEE Proceedings- , Volume: 147

Issue: 3 , May 2000

Page(s): 159 -165

[\[Abstract\]](#) [\[PDF Full-Text \(556 KB\)\]](#) **IEE JNL**

### 2 Object oriented development method for reconfigurable embedded systems

*Green, P.N.; Edwards, M.D.;*

Computers and Digital Techniques, IEE Proceedings- , Volume: 147

Issue: 3 , May 2000

Page(s): 153 -158

[\[Abstract\]](#) [\[PDF Full-Text \(520 KB\)\]](#) **IEE JNL**

### 3 CRUSADE: hardware/software co-synthesis of dynamically reconfigurable heterogeneous real-time distributed embedded systems

*Dave, B.P.;*

Design, Automation and Test in Europe Conference and Exhibition

1999. Proceedings , 1999

Page(s): 97 -104

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### 1 A systematic approach to software peripherals for embedded systems

*Lioupis, D.; Papagiannis, A.; Psihogiou, D.;*  
Hardware/Software Codesign, 2001. CODES 2001. Proceedings of the Ninth International Symposium on , 2001  
Page(s): 140 -145

[\[Abstract\]](#) [\[PDF Full-Text \(544 KB\)\]](#) **IEEE CNF**

### 2 Reengineering legacy embedded systems

*Madisetti, V.K.; Jung, Y.-K.; Khan, M.H.; Kim, J.; Finnessy, T.;*  
Design & Test of Computers, IEEE , Volume: 16 Issue: 2 , Apr-Jun 1999  
Page(s): 38 -47

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**61 ReMM: a Statecharts based modeling method for embedded systems**

*Jokela, T.;*

Computers and Communications, 1991. Conference Proceedings., Tenth Annual International Phoenix Conference on , 27-30 Mar 1991  
Page(s): 376 -382

[\[Abstract\]](#) [\[PDF Full-Text \(560 KB\)\]](#) **IEEE CNF**

**62 Structural way of thinking as applied to the design of embedded systems**

*Koono, Z.; Harada, E.;*

Communications, 1992. ICC 92, Conference record, SUPERCOMM/ICC '92, Discovering a New World of Communications. IEEE International Conference on , 14-18 Jun 1992  
Page(s): 1680 -1687 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(480 KB\)\]](#) **IEEE CNF**

**63 Designing databases in real-time embedded systems**

*Lin, K.-J.;*

Computer-Aided Control System Design, 1992. (CACSD), 1992 IEEE Symposium on , 17-19 Mar 1992  
Page(s): 285 -292

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[Print Format](#)**1 Embedded controllers, field bus and a modular I/O concept: central elements of BESSY II controls***Bergl, J.; Kuner, B.; Lange, R.; Muller, I.; Muller, R.; Pfeiffer, G.; Rahn, J.; Rudiger, H.;*Particle Accelerator Conference, 1997. Proceedings of the 1997 ,  
Volume: 2 , 12-16 May 1997

Page(s): 2493 -2495 vol.2

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